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FACSIMILE COVER SHEET

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MAR 5 2003

TECHNOLOGY CENTER 2800

2827

Art Group:

03-872-9318 Date: March 5, 2003				
From: Gregory D. Caldwell, Reg. No. 39,926				
Number of pages _10_including this sheet. Filing Date: 9/25/2001				
			Docket Due Date(s): 3/7/2003	
☐ Issue Fee Transmittal				
■ Notice of Appeal				
Petition for:				
Request for Continued Examination (RCE)				
Reply Brief (pgs)				
Request & Certification Under 35 USC 122(b)(2)(B)(i)				
☐ Request to Rescind Previous Nonpublication Request				
Response to Notice of Missing Parts & Formalities Letter				
☐ Response to Written Opinion (pgs)				
☐ Terminal Disclaimer				
☐ Transmittal of Publication Fee Due				
Transmittal Letter				

CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8A)

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Annie G. Pearson Date

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Under the Paperwork Recubion Acc		Application No.	09/965,234		
TRANSMITTAL FORM			September 25, 2001		
		Filing Date			
(to be used for all correspondence after initial filing)		First Named Inventor	Jarret L. Rinaldi		
		Group Art Unit	2827		
		Examiner Name	James M. Mitchell		
Total Number of Pages In This Submission 10		Attorney Docket Number	42390P12122		
ENCLOSURES (check all that apply)					
Fee Transmittal Form	Assignme (for an Ap	nt Papers plication)	After Allowance Communication to Group		
Fee Attached	☐ Drawing(s)	Appeal Communication to Board of Appeals and Interferences		
Amendment / Response	Licensing	related Papers	Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)		
☐ After Final ☐ Affidavits/declaration(s)	Petition		Proprietary Information		
Extension of Time Request	Provisiona	Convert a al Application	Status Letter Other Enclosure(s)		
Express Abandonment Request	Power of Change of	Attorney, Revocation f Correspondence Address	(please identify below):		
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Basic Filing Fee			MAR 5 2003		
Declaration/POA					
Response to Missing Parts under 37 CFR 1.52 or 1.53 Response to Missing TECHNOLOGY CENTER 2					
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT					
Firm Gregory D. Caldwell, Reg. No. 39,926					
or Individual name BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP					
Signature					
Date March 5, 2003					
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Typed or printed name Annie G. Pearson					
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U.S. Patent and Trademerk Office: U.S. DEPARTMENT OF COMMERCE
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FEE TRANSMITTAL	Application Number 09/965,234			
LEE I KANOMII I VE	25 2001			
for FY 2003	Filing Date September 23, 2001 First Named Inventor Jarret L. Rinaldi			
Effective 01/01/2003. Patent fees are subject to annual revision.	Examiner Name James M. Mitchell			
Applicant claims small entity status. See 37 CFR 1.27.				
—	Group/Art Offic			
TOTAL AMOUNT OF PAYMENT (\$) 410.00				
METHOD OF PAYMENT (check one)	FEE CALCULATION (continue) AX RECEIVE			
	3. ADDITIONAL FEES MAR 5 2003			
☐ Check ☐ Credit card ☐ Money ☐ Other ☐ None	Large Entity Small Entity MAR 5 2001			
Deposit Account -				
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Charge any additional fee(s) required under 37 CFR §§ 1.16, 1.17, 1.18 and 1.20	1805 1,840* 1806 1,840 Requesting publication of Sirk alies Examiner action			
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to the above-identified deposit according	1252 410 2252 205 Extension for reply within second month 410.00			
FEE CALCULATION	1253 930 2253 465 Extension for reply within third month			
1. BASIC FILING FEE	1254 1,450 2254 725 Extension for reply within fourth month			
Large Entity Small Entity Fee Fee Fee Description Fee Paid	1255 1,970 2255 985 Extension for reply within fifth month			
Fee Fee Fee Fee Fee Description Code (\$) Code (\$)	1404 320 2401 160 Notice of Appeal			
750 and 375 Utility filing fee	1402 320 2402 160 Filling a brief in support of an appeal			
1001 750 2001 375 Ctall Ing fee	1403 280 2403 140 Request for oral hearing			
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1202 18 2202 9 Claims in excess of 20	1810 750 2810 375 For each additional invention to be examined (37 CFR § 1.129(b))			
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SUBMITTED BY	1 (502) 694 6200			
Name (Print/Type) Gregory D. Caldwell	(Attorcey/Agent)			
1///	Date 03/05/03			
Signature Male	as the who which is to file (and by the USPTO to process) an			

This collection of information is required by 37 CFR 1. # erfd 1.27. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gethering, preparing, and submitting the application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gethering, preparing, and submitting the application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gethering, preparing, and submitting the application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gethering, preparing, and submitting the application. Complete this form and the required to complete this form and the required to complete this form and the complete this form and the required to complete the understand the required to complete the understand the understand the required to complete the understand the required to complete the understand the u

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Jarrett L. RINALDI, et al.

Serial No.:

09/965,234

Group Art Unit:

2827

Filed:

September 25, 2001

Examiner:

J. Mitchell

FOR:

MULTI-STACK SURFACE MOUNT LIGHT EMITTING DIODES

AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.111

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Assistant Commissioner for Patents Washington, DC 20231

MAR 5 2003

TECHNOLOGY CENTER 2800

Sir:

In response to the Office Action mailed October 7, 2002, for which the response period is extended two months by the attached petition for extension of time, please amend the application as follows:

IN THE CLAIMS:

Please cancel claims 1-12 without prejudice or disclaimer and amend the remaining claims as follows:

-- 21. (Amended) A surface mount light emitting diode package, comprising:
 an integrated circuit substrate;

a plurality of light emitting diode die formed on the integrated circuit substrate; and

a plurality of electrical contacts on the outside of the integrated circuit substrate, said contacts adapted for surface mounting to a circuit board and providing electrical signals to said plurality of light emitting diode die.